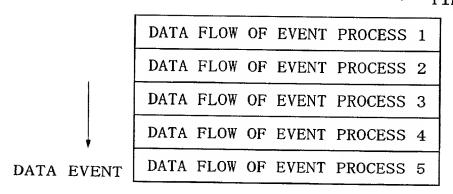
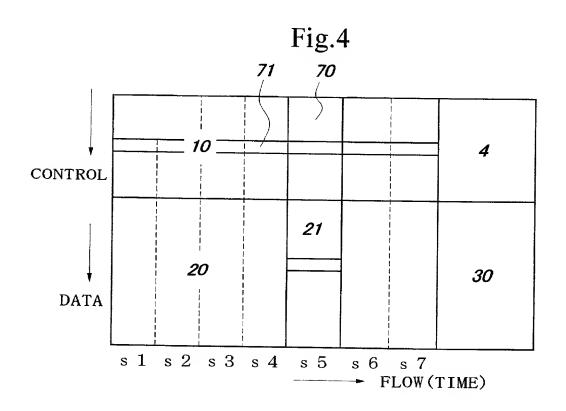


Fig.3

→ TIME (FLOW)





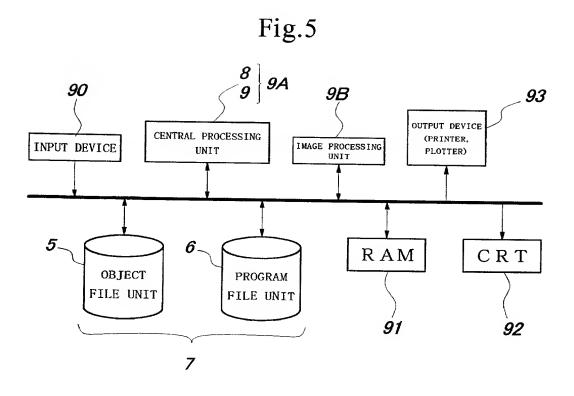


Fig.6

DERIVATION	VE C	LASS	NAMES	BAS	SIC	(CLA	S	S NA	MES					
ACCESS TYPE 1 1	TYPE 1	EXTERNAL	VARIABLE 1	ACCESS	TYPE	A	TYPE	A	MEMBER	FUNCTION	1	(түре	а	ARGUMENT	а)
ACCESS TYPE 2 T	YPE 2	EXTERNAL	VARIABLE 2	ACCESS	TYPE	В	TYPE	В	MEMBER	FUNCTION	ı	(TYPE	ь ь	ARGUMENT	b)
ACCESS TYPE 3 T	YPE 3	EXTERNAL	VARIABLE 3	ACCESS	ТҮРЕ	С	TYPE	c	MEMBER	FUNCTION	1	(TYPE	с	ARGUMENT	c)
ACCESS TYPE 4 T	YPE 4	EXTERNAL	VARIABLE 4	ACCESS	TYPE	D	TYPE	D	MEMBER	FUNCTION	1	(TYPE	d.	ARGUMENT	d)
•	•		•	•			•		•		•	•			
•	•		•	•			•		•		•	•			
•	•		•	•			•		•		•	•			

Fig.7

TYPE C MEMBER FUNCTION 3			
THE C MEMBER FUNCTION 3			
		-	
TYPE 1 EXTERNAL VARIABLE NAME 1			
TYPE 2 EXTERNAL VARIABLE NAME 2			
TYPE 3 EXTERNAL VARIABLE NAME 3			
TYPE 4 EXTERNAL VARIABLE NAME 4			
•			
•			
•			
TYPE c ARGUMENT c			
•			
•	İ		
•			
TYPE t INTERNAL VARIABLE t			
•			
• •			
•			

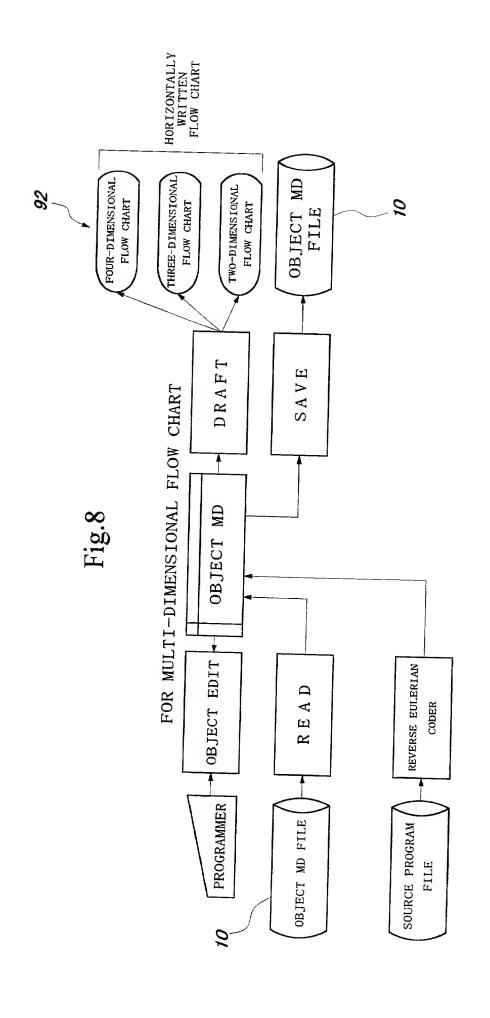
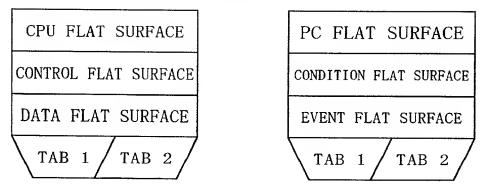
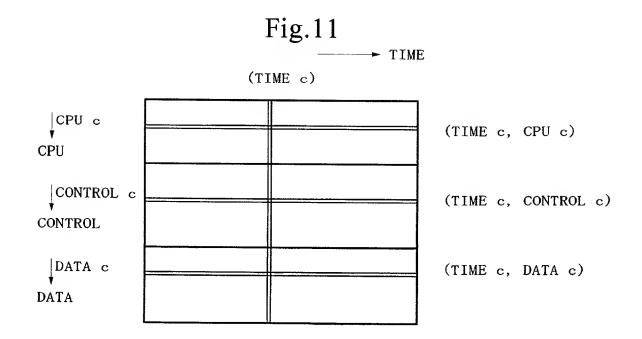


Fig.9

	TIME 1	TIME 2	TIME 3	TIME 4	TIME 5	TIME 6	TIME 7			
PC 1				1,6,2 %	X.X.	V,3%:		1		
PC 2										
PC 3								1		
PC 4								1		
CONDITION 1						(864.5%)	: 5: 4: \ :			
CONDITION 2				<u> </u>			<u> </u>	İ		
CONDITION 3					7.1					
CONDITION 4										
EVENT 1							318			
EVENT 2								1		
EVENT 3										
EVENT 4				-						
EVENT 5										,
CPU 1			,	· · · · · · · · · · · · · · · · · · ·			eyeni.			
CPU 2										
CPU 3			***************************************							
CONTROL 1	S. S.			\$: XX 3			
CONTROL 2						***************************************	••••••			
CONTROL 3								CONTROL 3	CONTROL	CONTROL
DATA 1										
DATA 2						***************************************	·····························			12:13.831.001
DATA 3										
DATA 4										
DATA 5										

Fig.10





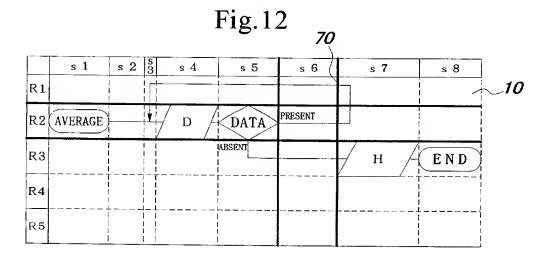


Fig. 13

	s 1	s 2	s 4	s 5	s 6	s 7	s 8	
D1			INPUT-	D				-20
D2		O→S -			D+S→S			
D3		O→C			1+C→C			
D4			<u> </u>					
D5								
D6			1					
				L				

Fig.14

R 4	R 3	R 2	R 1	
		D		D 1
		S		D 2
	Н	С		D 3
				D 4
				D 5
				D 6

Fig.15

	EVENT 1	EVENT 2	EVENT 3	EVENT 4	EVENT 5
STATE 1					
STATE 2					
STATE 3					

Fig.16

		TIME (FLOW)
DATA FLOW UNTIL STATE 1	DATA FLOW FOR EVENT PROCESS 1	DATA FLOW AFTER EVENT PROCESS ENDS
	DATA FLOW FOR EVENT PROCESS 2	
	DATA FLOW FOR EVENT PROCESS 3	
•	DATA FLOW FOR EVENT PROCESS 4	
DATA EVENT	DATA FLOW FOR EVENT PROCESS 5	

Fig.17

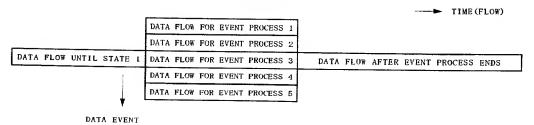


Fig.18

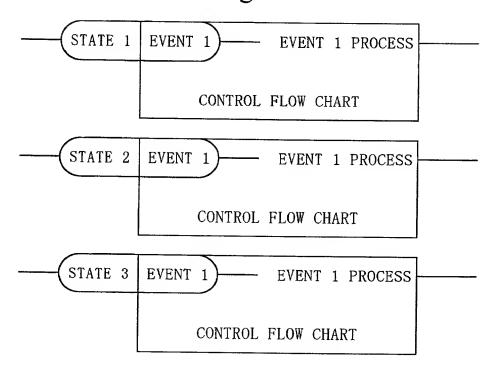


Fig.19

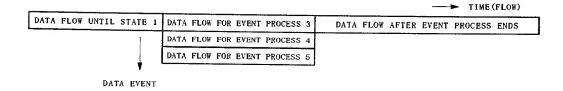
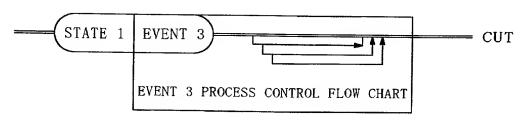
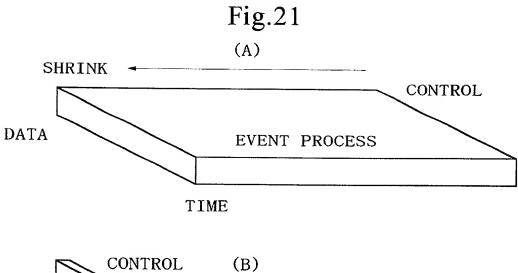


Fig.20





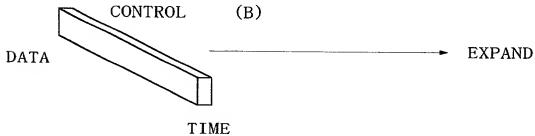


Fig.22

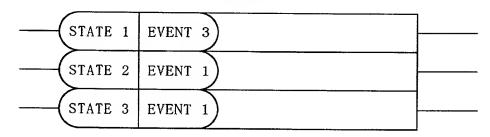


Fig.23

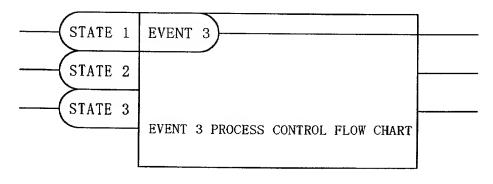


Fig.24

	TIME 1	TIME 2	TIME 3	TIME 4	TIME 5	TIME 6	TIME 7	TIME 8	TIME 9	TIME 10
CONTROL 1										
CONTROL 2			_							
CONTROL 3					PROC	ESS FC	R EVE	VIT 3		
CONTROL 4					rico	20010	, K L V L I			
CONTROL 5										
CONTROL 6					DPOC	ESS FO				
CONTROL 7					FROC	ESS FU	KEVE	NI I		
CONTROL 8										
CONTROL 9					DDOC	ESS FO	ים עם מ	UT 1		
CONTROL 10					PROC	E35 FU	REVE	NI I		
CONTROL 11										
CONTROL 12										<u> </u>

Fig.25

	TIME I	TIME 2	TIME 3	TIME 4	TIME 5	TIME 6	TIME 7	TIME 8	TIME 9	TIME 10
CONTROL 1										<u> </u>
CONTROL 2						-,, <u>-</u>				
CONTROL 3					PROC	CESS FO	OR EVE	NT 3		
CONTROL 6					PROC	CESS FO	OR EVE	NT 1		-
CONTROL 9					PROC	CESS FO	OR EVE	NT 1		
CONTROL 12										
CONTROL 13										
CONTROL 14										
CONTROL 15										
CONTROL 16						L				
CONTROL 17										
CONTROL 18										<u> </u>

Fig.26

TIME

CONTROL 1

CONTROL 2

CONTROL 3

CONTROL 4

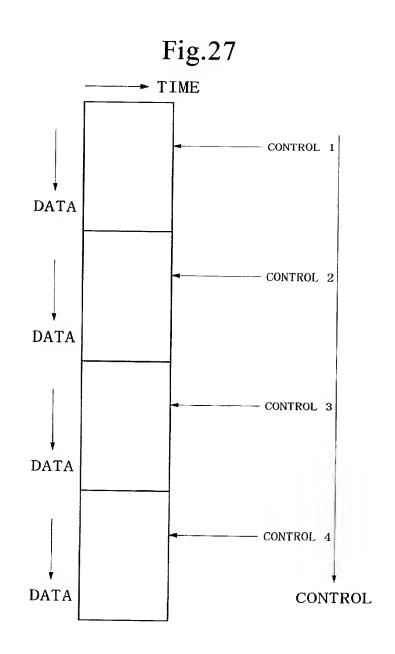


Fig.28

		TIME 1	TIME 2	TIME 3	TIME 4	TIME 5	TIME 6	TIME 7	TIME 8
CPU 1				****					
	CONTROL 1								
	DATA 1							·	
	DATA 2								
	CONTROL 2								
	DATA 1								
	DATA 2								
	CONTROL 3								
	DATA 1				-				
	DATA 2								
CPU 2									
1	CONTROL 1								
	DATA 1								
	DATA 2								
	CONTROL 2								
	DATA 1								
	DATA 2								
	CONTROL 3								
	DATA 1								
	DATA 2				-	-			

Fig.29

	TIME 1	TIME 2	TIME 3	TIME 4	TIME 5	TIME 6	TIME 7
CPU1	oru i						
CPU2		,				38: 8:	
CPU3							
CPU4							

Fig.30

	TIME 1	TIME 2	TIME 3	TIME 4	TIME 5	TIME 6	TIME 7
CPU1							
CPU2	ere i		CHU2			CPU1.	
CPU3							
CPU4							

Fig.31

